

A Parallel Algorithm for Time-Slot Assignment Problems in TDM Hierarchical Switching Systems

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Abstract—This paper presents a parallel algorithm for time-slot assignment problems in TDM hierarchical switching systems, based on the neural network model. The TDM systems are operated in repetitive frames composed of several time-slots. A time-slot represents a switching configuration where one packet is transmitted through an *I/O* line. The goal of our algorithm is to find conflict-free time-slot assignments for given switching demands. The algorithm runs on a maximum of $n^2 \times m$ processors for m -time-slot problems in $n \times n$ TDM systems. In small problems up to a 24×24 TDM system, the algorithm can find the optimum solution in a nearly constant time, when it is performed on $n^2 \times m$ processors.

I. INTRODUCTION

TDM (Time Division Multiplexing) hierarchical switching systems have been used widely in both terrestrial and satellite networks. The TDM systems can reduce hardware costs, enhance trunk line efficiency, and accommodate additional loads [1]–[4]. Eng *et al.* defined the general structure of TDM systems as shown in Fig. 1 [3]. A TDM system consists of three synchronously operated stages: several multiplexers, one central time-multiplex crossbar switch, and several demultiplexers. Multiplexer #*i* concentrates P_i inputs from end users to I_i TDM lines, then to the central switch, where P_i is usually larger than I_i . The central switch exchanges the TDM lines. Demultiplexer #*j* connects O_j TDM lines with Q_j outputs to end users, where Q_j is usually larger than O_j . The total number of input lines is equal to that of output lines.

We use the assumption of Eng *et al.* that signals through the TDM systems are fixed-length message packets [3]. The packets are transmitted in repetitive frames composed of several time-slots. A time-slot represents one switching configuration having a unit time, where one packet can be transmitted through one *I/O* line. As defined by Bonuccelli [4], packet transmission demands through the $n \times n$ TDM system are represented by an $n \times n$ traffic matrix. The ij th element t_{ij} represents the number of packets to be transmitted from input #*i* to output #*j*. t_{ij} is equivalent to the number of time-slots required for the packet transmission. In order to maximize

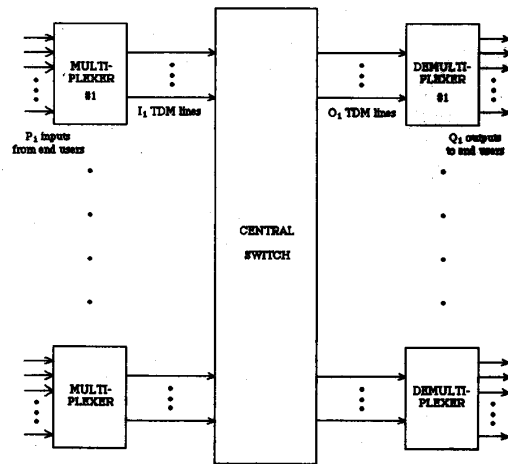


Fig. 1. A TDM hierarchical switching system.

the throughput, it is necessary to find the conflict-free time-slot assignment for a given traffic matrix using the minimum number of time-slots.

The problem of finding conflict-free time-slot assignments for the traffic matrix has been studied both in TDM hierarchical switching systems [3], [4], and in TDM non-hierarchical switching systems [5]–[15], particularly in TDMA (Time Division Multiple Access) satellite network applications. Although the time-slot assignment problem in hierarchical systems is not NP-complete, some problems in nonhierarchical systems have proven to be NP-complete. In 1983, Gopal *et al.* proved that the traffic scheduling problem in the case of zone interference, is NP-complete [13]. In 1985, Gopal *et al.* proved that the minimization problem of switching configuration chances is NP-complete [14]. In 1987, Bertossi *et al.* proved that the minimization problem of total transmission time is NP-complete [15]. The nonhierarchical system can be seen as a special case of the hierarchical system, because the former system does not involve multiplexers and demultiplexers. In 1987 Eng *et al.* showed the four requisite conditions for valid solutions in time-slot assignment problems in hierarchical systems [3]. In 1989, Bonuccelli proposed the $O(n^5)$ sequential algorithm for $n \times n$ hierarchical systems [4]. Also in 1989, Rose proposed the $O(n)$ parallel algorithm on n^2 processing elements for simple $n \times n$ crossbar switching systems, based on a cellular automaton [16]. Because Rose's algorithm cannot deal with

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multiplexer/demultiplexer constraints, a lot of modifications are required to cope with time-slot assignment problems in TDM hierarchical switching systems. A fast parallel algorithm is essential to improve the switching performance.

II. NEURAL NETWORK APPROACH FOR OPTIMIZATION PROBLEMS

Our parallel algorithm is based on the three-dimensional artificial neural network model. A neural network model for solving optimization problems consists of a large number of interconnected processing elements. These processing elements are called neurons because they perform the function of simplified biological neurons. The interconnections between processing elements are given by the motion equation:

$$\frac{dU_{ijk}}{dt} = -\frac{\partial E}{\partial V_{ijk}} \quad (1)$$

where U_{ijk} and V_{ijk} are the input and the output respectively of processing element # ijk . The computational energy function E represents all the constraints of the optimization problem. The goal of the neural network approach is to minimize energy function E , where the motion equation performs the gradient descent method [22].

In 1943 McCulloch and Pitts proposed the first mathematical neuron model [17]. The input/output function of processing element # ijk is given by

$$\begin{aligned} \text{if } U_{ijk} > 0 \text{ then } V_{ijk} &= 1 \\ \text{if } U_{ijk} \leq 0 \text{ then } V_{ijk} &= 0. \end{aligned} \quad (2)$$

Hopfield *et al.* first introduced the neural network model for solving optimization problems by using the sigmoid neuron model [18]. The input/output function of processing element # ijk is given by

$$V_{ijk} = \frac{1}{1 + \exp\left(-\frac{U_{ijk}}{U_0}\right)} \quad (3)$$

where U_0 is a constant parameter.

In 1989 Marrakchi *et al.* proposed the Hopfield neural network model application for crossbar switching systems, which was verified only in an 8×8 crossbar system [19]. In 1989 Brown proposed the Hopfield neural network model application for multistage crossbar switching systems [20]. Also in 1990, Brown *et al.* proposed the application for Banyan network systems [21]. Unfortunately, none of these investigators discussed the time complexity and the convergence frequency of neural network models, although these factors are always controversial in neural network research. Takefuji *et al.* proved that the decay term in the Hopfield neural network model disturbs the convergence under some conditions [22].

Although the McCulloch-Pitts neuron model can drastically reduce the computation time on a digital computer, it

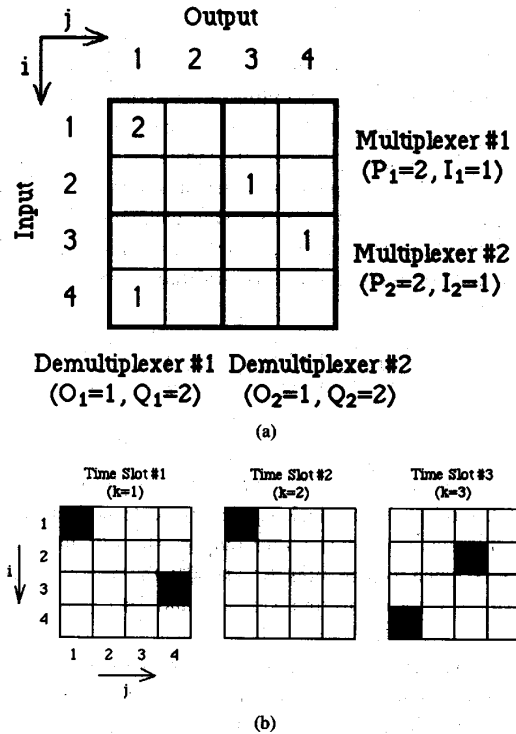


Fig. 2. A 4×4 traffic matrix problem and the neural network representation. (a) A 4-input 4-output traffic matrix problem. (b) The neural network representation with 48 processing elements.

sometimes introduces undesirable oscillatory behavior. It has been shown empirically that the newly introduced hysteresis McCulloch-Pitts neuron model can suppress this oscillatory behavior [24]. The input/output function of processing element # ijk is given by:

$$\begin{aligned} \text{if } U_{ijk} > UTP \text{ then } V_{ijk} &= 1 \\ \text{if } U_{ijk} < LTP \text{ then } V_{ijk} &= 0 \\ \text{otherwise } V_{ijk} &= \text{unchanged}. \end{aligned} \quad (4)$$

where UTP is always larger than LTP , and the initial value of V_{ijk} must be assigned 1 or 0.

III. NEURAL NETWORK REPRESENTATION FOR TIME-SLOT ASSIGNMENT PROBLEMS

Fig. 2(a) shows the neural network model for the 4×4 traffic matrix problem. Each multiplexer connects two inputs ($P_i = 2$) with one TDM line ($I_i = 1$). The central switch exchanges two TDM lines. Each demultiplexer connects one TDM line ($O_i = 1$) with two outputs ($Q_i = 2$). Fig. 2(b) shows the neural network representation for the three-time-slot problem of Fig. 2(a). Because the assignment of each traffic matrix element requires three processing elements, a total of 48 ($= 4 \times 4 \times 3$) processing elements is prepared in this problem.

Generally, a total of $n^2 \times m$ processing elements is required for the m -time-slot problem in an $n \times n$ TDM system. The output of processing element $\#ijk$ represents whether or not one packet from input $\#i$ to output $\#j$ is assigned to time-slot $\#k$. The nonzero output ($V_{ijk} = 1$) indicates the assignment, and the zero output ($V_{ijk} = 0$) indicates no assignment. Because a total of t_{ij} packets is demanded by traffic matrix element $\#ij$, a total of t_{ij} processing elements from among m processing elements for t_{ij} , must have nonzero output. The energy function representing this constraint is given by

$$E_1 = \left(\sum_{r=1}^m V_{ijr} - t_{ij} \right)^2 \quad (5)$$

E_1 is zero if, and only if, every packet is assigned to a time-slot. Fig. 2(b) also shows the convergence state of the neural network model, where black squares indicate nonzero output and white squares indicate zero output. The two packets for t_{11} are assigned to time-slots $\#1$ and $\#2$, the packet for t_{23} to time-slot $\#3$, for t_{34} to time-slot $\#1$, and for t_{41} to time-slot $\#3$.

The TDM hierarchical switching system has two constraints: 1) in a given time-slot, each input can be connected with only one output and each output with only one input (point-to-point connections) and 2) in a given time-slot, multiplexer $\#y$ can connect at most I_y inputs with the central switch, and demultiplexer $\#z$ can connect at most O_z outputs with the central switch. The energy function representing the first constraint is given by:

$$E_2 = \sum_{i=1}^n \sum_{j=1}^n \sum_{k=1}^m \left(\sum_{\substack{p=1 \\ p \neq i}}^n V_{pjk} + \sum_{\substack{q=1 \\ q \neq j}}^n V_{iqk} \right) V_{ijk} \quad (6)$$

E_2 is zero if, and only if, at most one packet from an input and at most one packet to an output are assigned to any time-slot. The energy function representing the second constraint is given by

$$E_3 = \sum_{i=1}^n \sum_{j=1}^n \sum_{k=1}^m \left(f \left(\sum_{\substack{p \in y \\ (p,q) \neq (i,j)}} \sum_{q=1}^n V_{pqk} - I_y \right) + f \left(\sum_{\substack{q \in z \\ (p,q) \neq (i,j)}} \sum_{p=1}^n V_{pqk} - O_z \right) \right) V_{ijk} \quad (7)$$

where $f(x)$ is 1 if $x \geq 0$, and $f(x)$ is 0 if $x < 0$. E_3 is zero if, and only if, I_y or fewer inputs are assigned in any time-slot for multiplexer $\#y$, and O_z or fewer outputs are assigned in any time-slot for demultiplexer $\#z$. Note that input $\#i$ is connected with multiplexer $\#y$, and output $\#j$ with demultiplexer $\#z$.

TABLE I
SPECIFICATIONS AND SIMULATION RESULTS IN TEN PROBLEMS

	problem specification		simulation result	
	matrix size	time-slot #	average # of iteration steps	convergence frequency
Problem #1	6 × 6	7	845.3	95%
Problem #2	8 × 8	10	860.2	93%
Problem #3	10 × 10	9	705.8	91%
Problem #4	12 × 12	9	831.3	92%
Problem #5	14 × 14	10	662.9	94%
Problem #6	16 × 16	11	820.2	90%
Problem #7	18 × 18	12	765.2	82%
Problem #8	20 × 20	13	705.2	89%
Problem #9	22 × 22	14	816.5	80%
Problem #10	24 × 24	15	728.0	76%

The total energy function for this problem is given by:

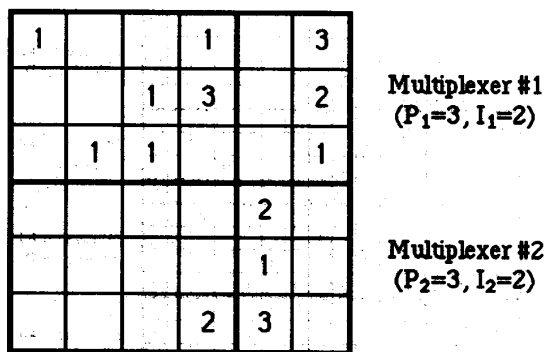
$$\begin{aligned} E &= \frac{A}{2} E_1 + B E_2 + C E_3 \\ &= \frac{A}{2} \left(\sum_{r=1}^m V_{ijr} - t_{ij} \right)^2 \\ &\quad + B \sum_{i=1}^n \sum_{j=1}^n \sum_{k=1}^m \left(\sum_{\substack{p=1 \\ p \neq i}}^n V_{pjk} + \sum_{\substack{q=1 \\ q \neq j}}^n V_{iqk} \right) V_{ijk} \\ &\quad + C \sum_{i=1}^n \sum_{j=1}^n \sum_{k=1}^m \left(f \left(\sum_{\substack{p \in y \\ (p,q) \neq (i,j)}} \sum_{q=1}^n V_{pqk} - I_y \right) \right. \\ &\quad \left. + f \left(\sum_{\substack{q \in z \\ (p,q) \neq (i,j)}} \sum_{p=1}^n V_{pqk} - O_z \right) \right) V_{ijk} \quad (8) \end{aligned}$$

where A , B , and C are constant coefficients.

From (1) and (8), the motion equation of processing element $\#ijk$ for the m -time-slot problem in the $n \times n$ TDM system is given by

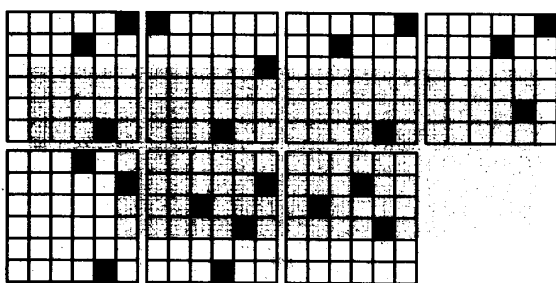
$$\begin{aligned} \frac{dU_{ijk}}{dt} &= -A \left(\sum_{r=1}^m V_{ijr} - t_{ij} \right) - B \left(\sum_{\substack{p=1 \\ p \neq i}}^n V_{pjk} + \sum_{\substack{q=1 \\ q \neq j}}^n V_{iqk} \right) \\ &\quad - C \left(f \left(\sum_{\substack{p \in y \\ (p,q) \neq (i,j)}} \sum_{q=1}^n V_{pqk} - I_y \right) \right. \\ &\quad \left. + f \left(\sum_{\substack{q \in z \\ (p,q) \neq (i,j)}} \sum_{p=1}^n V_{pqk} - O_z \right) \right) \quad (9) \end{aligned}$$

The A -term forces total t_{ij} from m processing elements for traffic matrix element t_{ij} , to have nonzero output. The B -term discourages processing element $\#ijk$ from having nonzero output if other demands from input $\#i$ and/or to output $\#j$ are



Demultiplexer #1 ($O_1=2, Q_1=4$) Demultiplexer #2 ($O_2=2, Q_2=2$)

(a)



(b)

Fig. 3. The 6 x 6 traffic matrix and the solution to problem #1.

assigned to time-slot #k. The C-term has the same role as the B-term if I_y or more packets sharing multiplexer #y, and/or O_z or more packets sharing demultiplexer #z, are assigned to time-slot #k.

IV. THREE HEURISTICS FOR THE GLOBAL MINIMUM CONVERGENCE

In the neural network model, only the local minimum convergence has been proven [22]. In order to improve the global minimum convergence, as required in optimization problems, we have introduced empirically the following three heuristics:

- 1) The hill-climbing term heuristic [22]

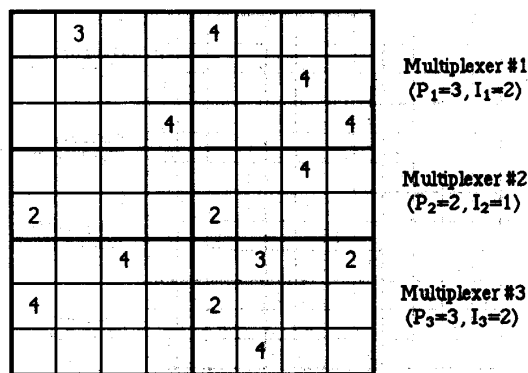
The following hill-climbing term is added to the motion equation, in order to help a total of t_{ij} processing elements for t_{ij} to have nonzero output, which is necessary in the global minimum solution of the neural network model:

$$+ Dh \left(\sum_{r=1}^m V_{ijr} - t_{ij} \right) \quad (10)$$

where $h(x)$ is 1 if $x < 0$, and $h(x)$ is 0 if $x \geq 0$.

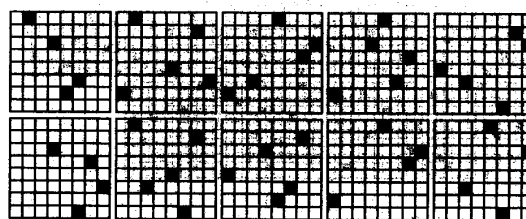
- 2) The omega function heuristic [23]

The periodic use of the following two forms of B-term in the motion equation makes local minima shallower, and allows



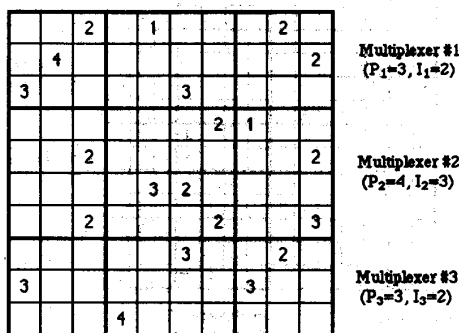
Demultiplexer #1 ($O_1=2, Q_1=4$) Demultiplexer #2 ($O_2=3, Q_2=4$)

(a)



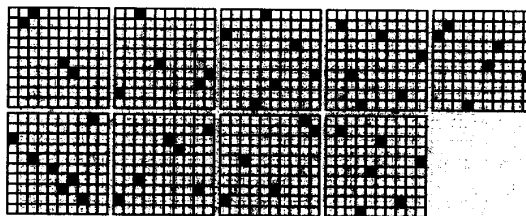
(b)

Fig. 4. The 8 x 8 traffic matrix and the solution to problem #2.



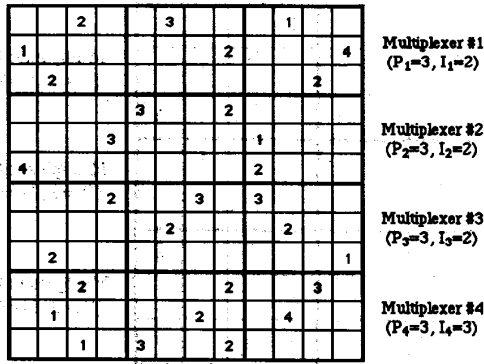
Demultiplexer #1 ($O_1=2, Q_1=3$) Demultiplexer #2 ($O_2=3, Q_2=4$) Demultiplexer #3 ($O_3=2, Q_3=3$)

(a)



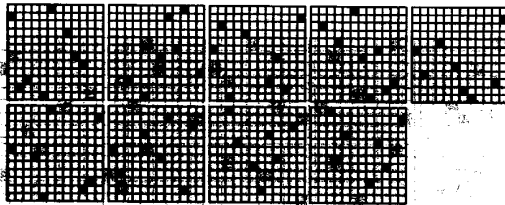
(b)

Fig. 5. The 10 x 10 traffic matrix and the solution to problem #3.



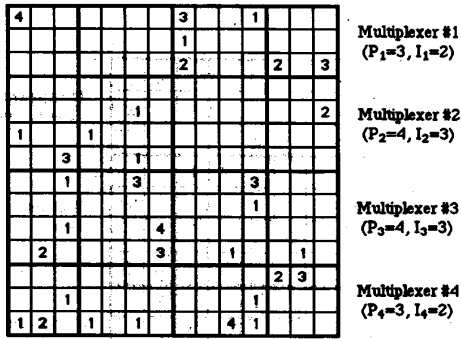
Demultiplexer #1 (O₁=3, Q₁=4) Demultiplexer #2 (O₂=3, Q₂=4) Demultiplexer #3 (O₃=3, Q₃=4)

(a)



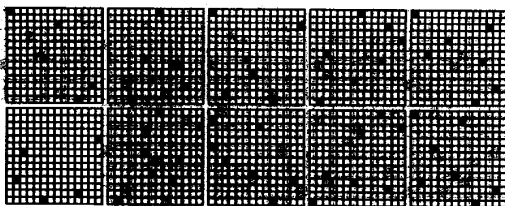
(b)

Fig. 6. The 12 x 12 traffic matrix and the solution to problem #4.



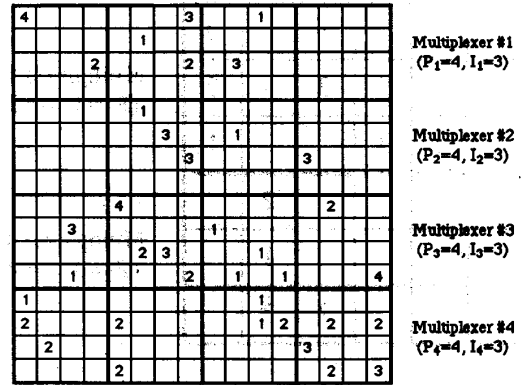
Demultiplexer #1 (O₁=2, Q₁=3) Demultiplexer #2 (O₂=3, Q₂=4) Demultiplexer #3 (O₃=3, Q₃=4) Demultiplexer #4 (O₄=2, Q₄=3)

(a)



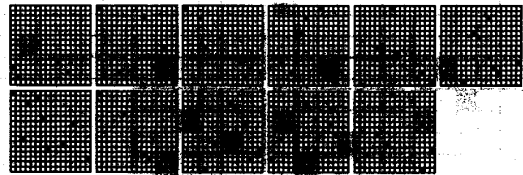
(b)

Fig. 7. The 14 x 14 traffic matrix and the solution to problem #5.



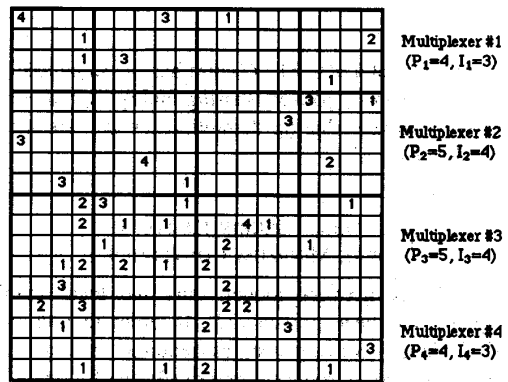
Demultiplexer #1 (O₁=3, Q₁=4) Demultiplexer #2 (O₂=3, Q₂=4) Demultiplexer #3 (O₃=3, Q₃=4) Demultiplexer #4 (O₄=3, Q₄=4)

(a)



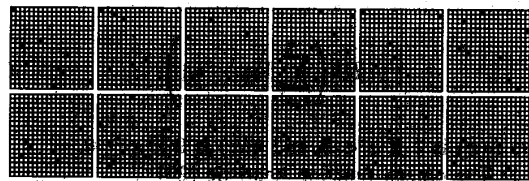
(b)

Fig. 8. The 16 x 16 traffic matrix and the solution to problem #6.



Demultiplexer #1 (O₁=3, Q₁=4) Demultiplexer #2 (O₂=4, Q₂=5) Demultiplexer #3 (O₃=4, Q₃=5) Demultiplexer #4 (O₄=3, Q₄=4)

(a)



(b)

Fig. 9. The 18 x 18 traffic matrix and the solution to problem #7.

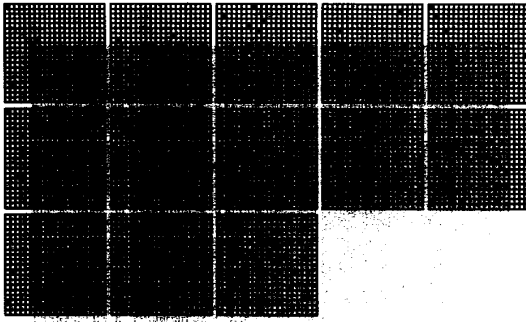
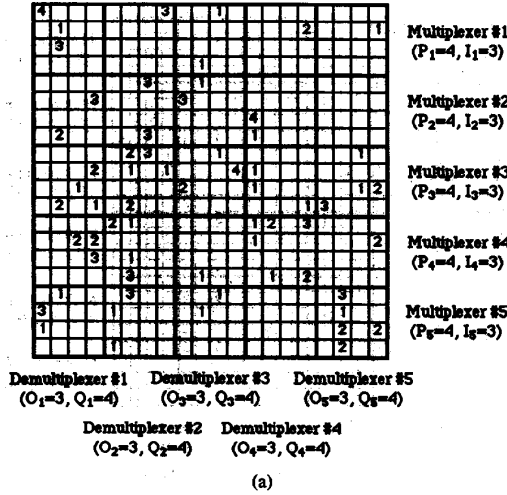


Fig. 10. The 20 × 20 traffic matrix and the solution to problem #8.

the neural network model to escape easily:

$$\text{if } (t \bmod T) < \omega \text{ then } -B \left(\sum_{\substack{p=1 \\ p \neq i}}^n V_{pjk} + \sum_{\substack{q=1 \\ q \neq j}}^n V_{iqk} \right) V_{ijk}$$

$$\text{else } -B \left(\sum_{\substack{p=1 \\ p \neq i}}^n V_{pjk} + \sum_{\substack{q=1 \\ q \neq j}}^n V_{iqk} \right) \quad (11)$$

where t is the number of iteration steps, and T and ω are omega function parameters.

3) The input saturation heuristic [25]

The neuron input is saturated between two values, which also makes local minima shallower.

$$\text{If } U_{ijk} > U_{\max} \text{ then } U_{ijk} = U_{\max}$$

$$\text{If } U_{ijk} < U_{\min} \text{ then } U_{ijk} = U_{\min} \quad (12)$$

where U_{\max} is the upper limit of the input, and U_{\min} is the lower limit.

V. PARALLEL ALGORITHM

The following procedure represents our parallel algorithm for the m -time-slot problem in the $n \times n$ TDM system. The

set of parameters are empirically determined [22]–[34].

$$0) \text{ Set } t = 0, A = B = C = 1, D = 20,$$

$$T = 5, \omega = 2, \text{UTP} = 5, \text{LTP} = -5,$$

$$U_{\max} = 200, U_{\min} = -800, \text{ and } T_{\max} = 3000. \quad (13)$$

1) Set initial values of $U_{ijk}(t)$ for $i = 1, \dots, n$, $j = 1, \dots, n$, and $k = 1, \dots, m$ by uniform random numbers between U_{\max} and U_{\min} , and set initial values of $V_{ijk}(t)$ by 0.

2) Use the motion equation with two heuristics to compute $\Delta U_{ijk}(t)$:
if $(t \bmod T) < \omega$ then

$$\Delta U_{ijk}(t) = -A \left(\sum_{r=1}^m V_{ijr}(t) - t_{ij} \right)$$

$$- B \left(\sum_{\substack{p=1 \\ p \neq i}}^n V_{pjk}(t) + \sum_{\substack{q=1 \\ q \neq j}}^n V_{iqk}(t) \right) V_{ijk}(t)$$

$$- C \left(f \left(\sum_{\substack{p \in y \\ (p,q) \neq (i,j)}} \sum_{q=1}^n V_{pqk}(t) - I_y \right) \right.$$

$$\left. + f \left(\sum_{\substack{q \in z \\ (p,q) \neq (i,j)}} \sum_{p=1}^n V_{pqk}(t) - O_z \right) \right)$$

$$+ Dh \left(\sum_{r=1}^m V_{ijr}(t) - t_{ij} \right)$$

else

$$\Delta U_{ijk}(t) = -A \left(\sum_{r=1}^m V_{ijr}(t) - t_{ij} \right)$$

$$- B \left(\sum_{\substack{p=1 \\ p \neq i}}^n V_{pjk}(t) + \sum_{\substack{q=1 \\ q \neq j}}^n V_{iqk}(t) \right)$$

$$- C \left(f \left(\sum_{\substack{p \in y \\ (p,q) \neq (i,j)}} \sum_{q=1}^n V_{pqk}(t) - I_y \right) \right.$$

$$\left. + f \left(\sum_{\substack{q \in z \\ (p,q) \neq (i,j)}} \sum_{p=1}^n V_{pqk}(t) - O_z \right) \right)$$

$$+ Dh \left(\sum_{r=1}^m V_{ijr}(t) - t_{ij} \right) \quad (14)$$

3) Update $U_{ijk}(t+1)$ based on the first-order Euler method:

$$U_{ijk}(t+1) = U_{ijk}(t) + \Delta U_{ijk}(t) \quad (15)$$

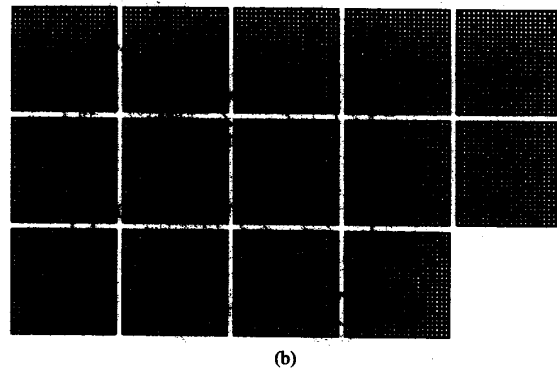
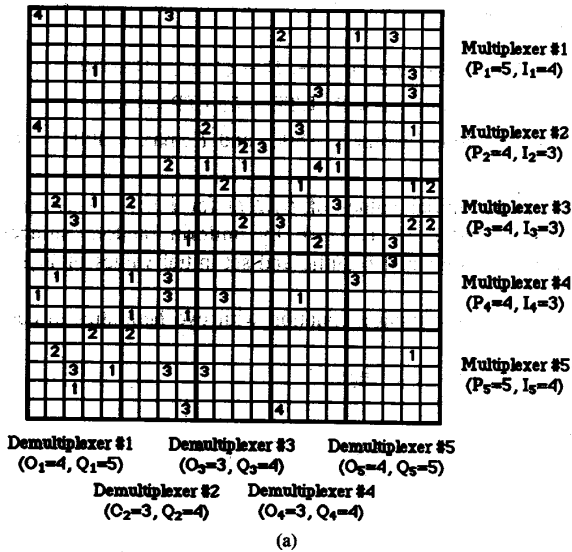


Fig. 11. The 22 × 22 traffic matrix and the solution to problem #9.

4) Use the input saturation heuristic:

$$\begin{aligned} \text{If } U_{ijk}(t+1) > U_{\text{-max}} \text{ then } U_{ijk}(t+1) &= U_{\text{-max}} \\ \text{If } U_{ijk}(t+1) < U_{\text{-min}} \text{ then } U_{ijk}(t+1) &= U_{\text{-min}} \end{aligned} \quad (16)$$

5) Update $V_{ijk}(t+1)$ based on the hysteresis McCulloch-Pitts neuron model:

$$\begin{aligned} \text{if } U_{ijk}(t+1) > UTP \text{ then } V_{ijk}(t+1) &= 1 \\ \text{if } U_{ijk}(t+1) < LTP \text{ then } V_{ijk}(t+1) &= 0 \end{aligned} \quad (17)$$

6) If all the constraints are satisfied ($E = 0$), or $t = T_{\text{-max}}$, then terminate this procedure, else increment t by 1, and go to step 2.

VI. SIMULATION RESULTS AND DISCUSSION

The simulator based on the algorithm has been developed on a Macintosh. The ten problems shown in Table I were

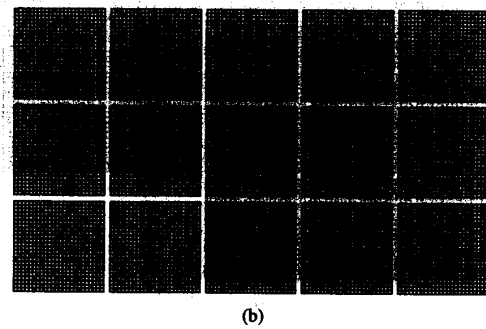
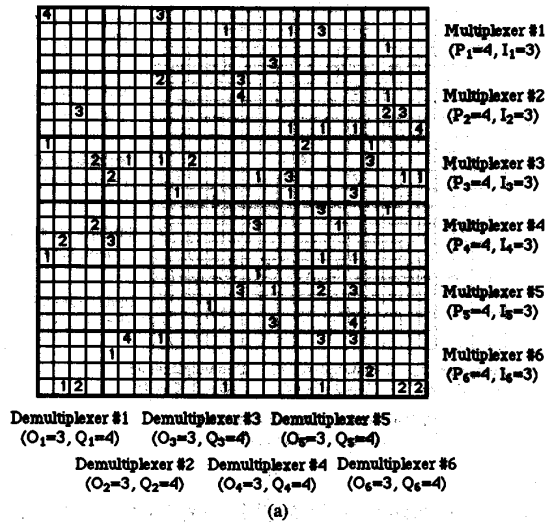


Fig. 12. The 24 × 24 traffic matrix and the solution to problem #10.

examined. Problem #1 was originally described by Gopal [14], where it was modified from a nonhierarchical to a hierarchical problem. Problem #2 was described by Bonuccelli [4]. Problems #3–#10 were newly created in this paper, where traffic matrix elements were randomly generated. Each problem satisfies the following conditions: 1) a maximum of four packets are demanded in any traffic matrix element, 2) the number of time-slots is the optimum condition to have valid solutions, and 3) the multiplexer/demultiplexer size is similar to problem #2.

Figs. 3–12 show traffic matrices and multiplexer/demultiplexer size, and the global minimum solutions found by our simulator in the ten problems. The simulator found several solutions to one problem from different initial values of U_{ijk} . In order to avoid initial value dependency of the neural network model, 100 simulation runs were performed from different initial values of U_{ijk} . Table I also summarizes the average number of iteration steps required for global minimum convergence, and the frequency. Fig. 13 shows the distribution of the number of iteration steps required for global minimum convergence in two problems. The simulation results show that in a nearly constant number of iteration steps, our algorithm can find optimum solutions, in small size problems

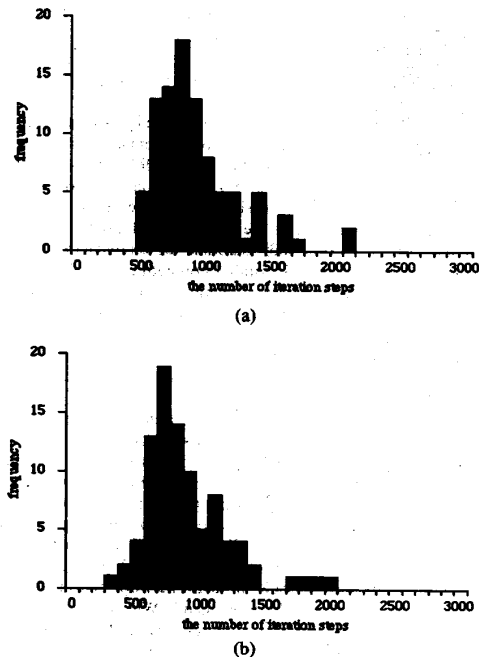


Fig. 13. The distribution of the number of iteration steps required for global minimum convergence (a) Problem #2. (b) Problem #6.

up to a 24×24 TDM system. We therefore conclude that our parallel algorithm for m -time-slot assignment problems in $n \times n$ TDM hierarchical switching systems, is able to find optimum solutions in a nearly constant time, when it is performed on a parallel machine using $n^2 \times m$ processors.

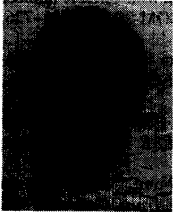
VII. CONCLUSION

Based on the neural network model composed of $n^2 \times m$ processing elements, we propose a parallel algorithm for m -time-slot assignment problems in $n \times n$ TDM hierarchical switching systems. The algorithm runs not only on a sequential machine, but also on a parallel machine with a maximum of $n^2 \times m$ processors. In small size problems (up to a 24×24 TDM system), our simulation results show that the algorithm is able to find optimum solutions in a nearly constant time, when it is performed on a parallel machine with $n^2 \times m$ processors. The algorithm can be easily modified for other time-slot assignment problems which have multipoint connections, and/or rectangular traffic matrices.

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